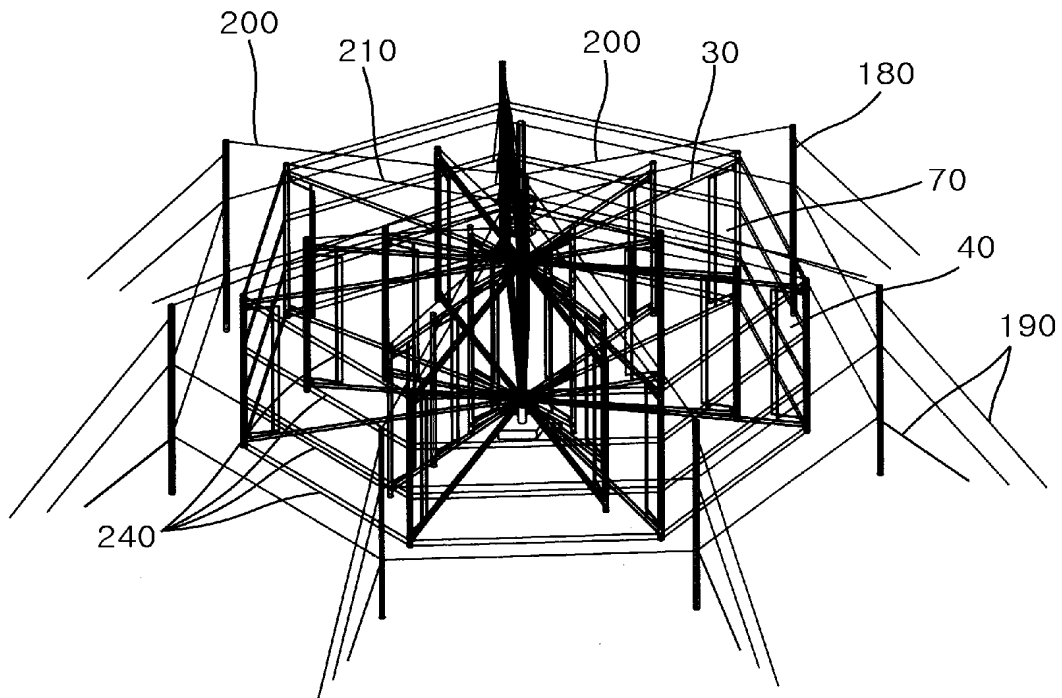


[Fig. 1]



[Fig. 2]

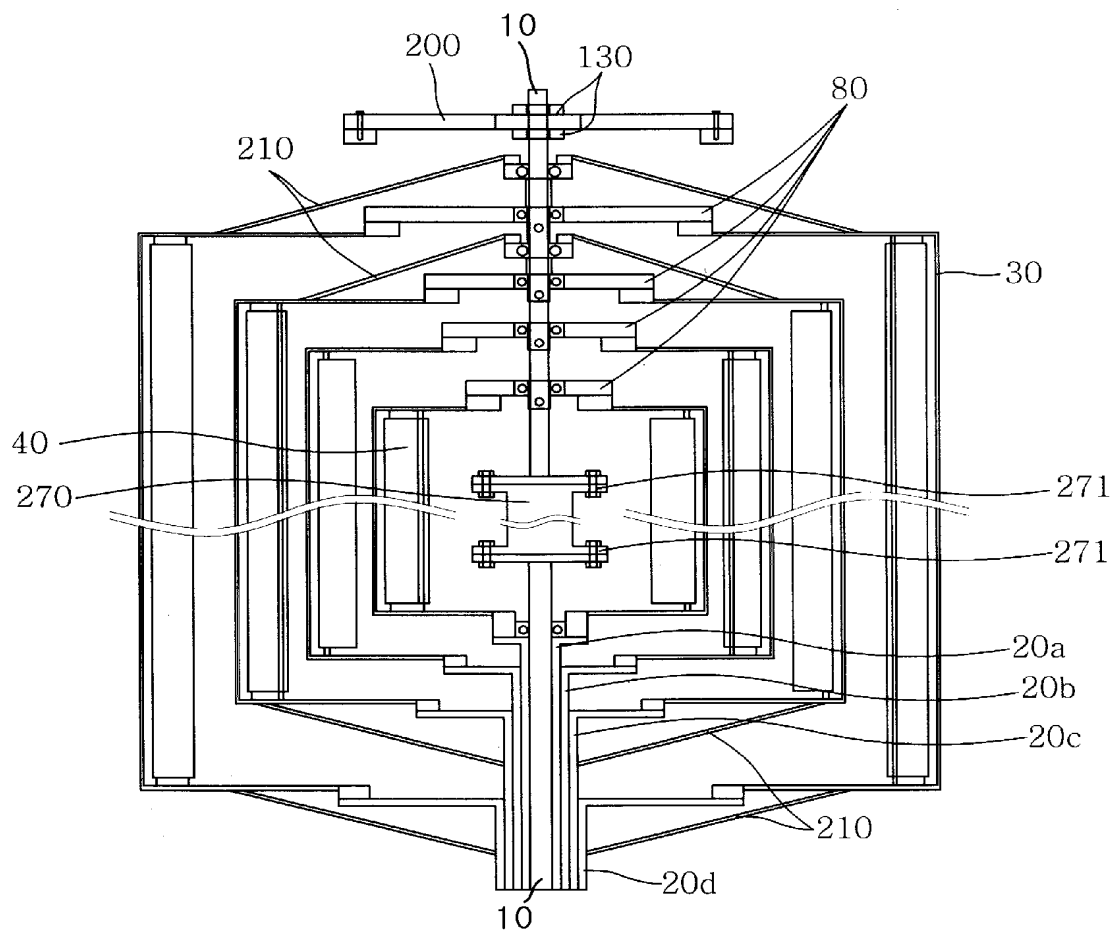
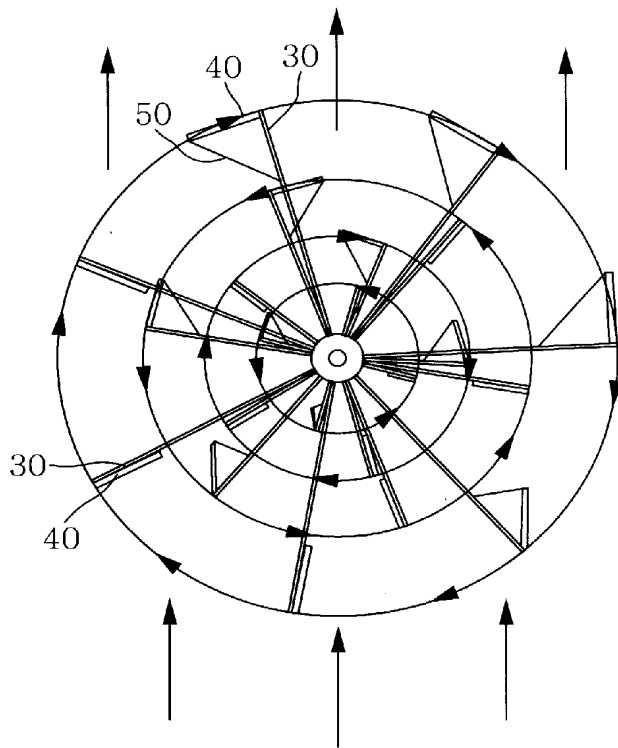
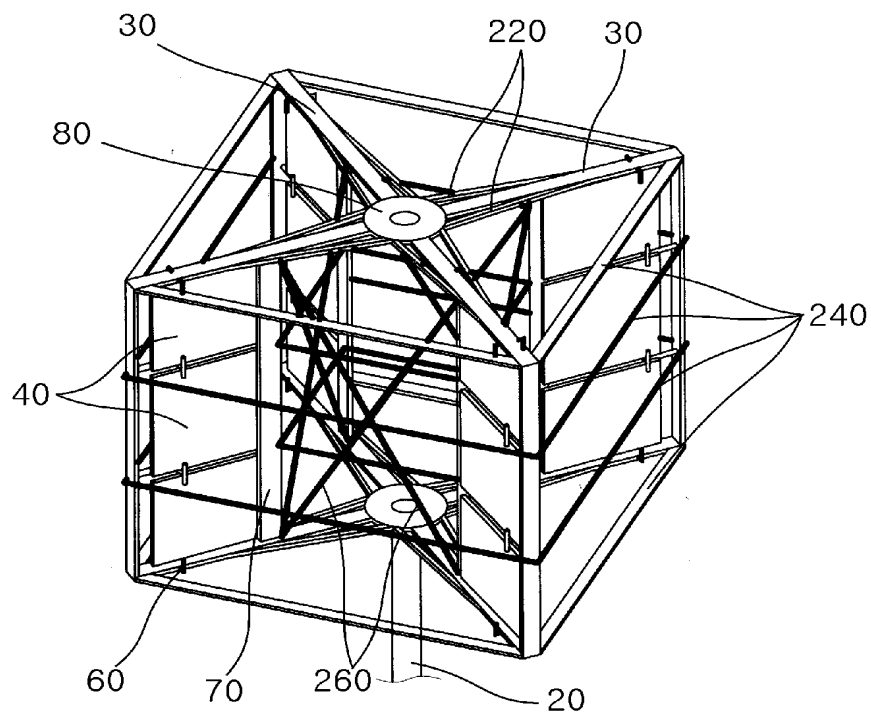


Figure 1 is a schematic diagram of a multi-layered printed circuit board (PCB) assembly. The diagram shows a central vertical core (10) with multiple layers (20a, 20b, 20c, 20d) and a top layer (21). The assembly is mounted on a base (120) and includes a top cover (130) and a bottom cover (140). A central vertical slot (150) is shown, and a component (160) is mounted on the right side. The diagram is labeled with various reference numerals: 10, 20a, 20b, 20c, 20d, 21, 120, 130, 140, 150, 160, 280, and A.

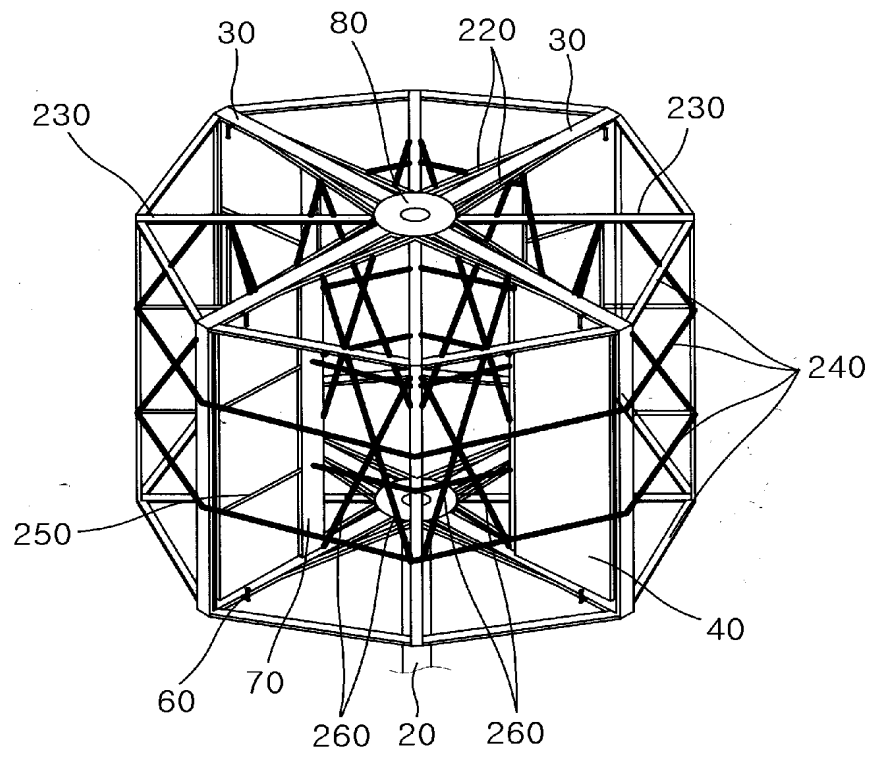
[Fig. 5]



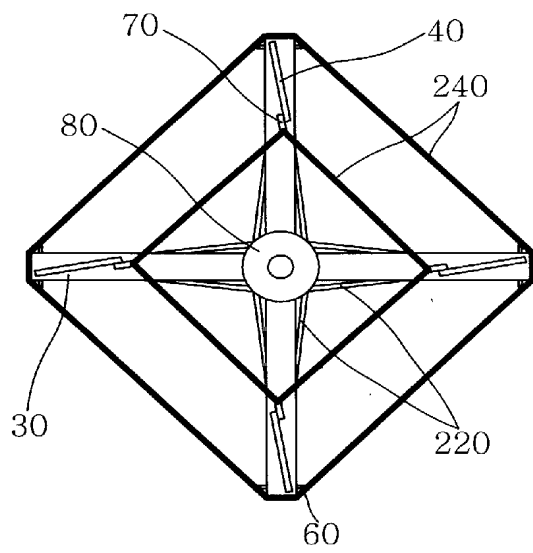
[Fig. 6]



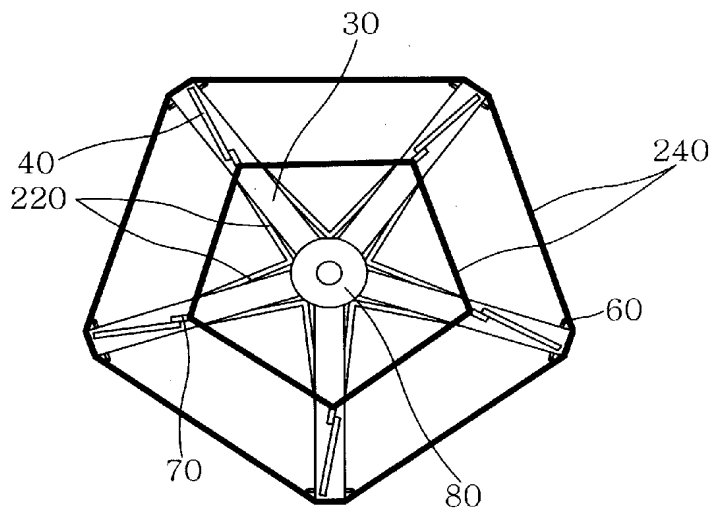
[Fig. 7]



[Fig. 8]



[Fig. 9]



[Fig. 10]

